

L Number	Hits	Search Text	DB	Time stamp
1	33	(dmos and interconnect and @pd>20030309) (dmos and cmos and @pd>20030309) ((inverter adj circuit) and (dmos and cmos) and @pd>20030309)	USPAT	2003/10/25 15:11
2	174	((257/327-329,331,351,508,509,903,904).CCLS.) and @pd>20030309	USPAT	2003/10/25 15:23
3	311	((((257/351,508,509,904).CCLS.) and cmos) and (metal silicide)) and @pd>19930713	USPAT	2003/10/25 16:47
4	921	((257/351,508,509,904).CCLS.) and @pd<20030310	USPAT	2003/10/25 16:47
5	256	(vertical adj replacement adj gate surround\$3 adj gate) and @pd>20030309	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/25 16:47
-	321	((((257/327-331,334,369,379,380,385,401,900,903) or (365/154,180,184) or (438/138,152-156,206,268)).CCLS.) and @pd>20020930	USPAT	2003/10/24 16:43
-	469	((((257/327-331,334,369,379,380,385,401,900,903) or (365/154,180,184) or (438/138,152-156,206,268)).CCLS.) and gate near3 (polysilicon poly) near6 (dope dopant doping impurity type) and @ad<20000825	USPAT; US-PGPUB	2003/03/10 14:33
-	451	((((257/327-331,334,369,379,380,385,401,900,903) or (365/154,180,184) or (438/138,152-156,206,268)).CCLS.) and gate near3 (polysilicon poly) near6 (dope dopant doping impurity type) and @ad<20000825	USPAT	2003/03/10 14:34
-	18	((((257/327-331,334,369,379,380,385,401,900,903) or (365/154,180,184) or (438/138,152-156,206,268)).CCLS.) and gate near3 (polysilicon poly) near6 (dope dopant doping impurity type)) not @ad<20000825	USPAT	2003/03/10 14:34
-	599	((((257/327-331,334,369,379,380,385,401,900,903) or (365/154,180,184) or (438/138,152-156,206,268)).CCLS.) and @pd>20020401	USPAT	2003/03/10 15:00
-	2253	vertical adj replacement adj gate (vrg and 257/\$3.ccls.) surround\$3 adj gate	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 16:47
-	36	(vertical adj replacement adj gate (vrg and 257/\$3.ccls.) surround\$3 adj gate) and (inverter adj circuit) and @ad<20000825	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/10 15:02
-	1794	(vertical adj replacement adj gate (vrg and 257/\$3.ccls.) surround\$3 adj gate) and @ad<20000825	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/10 15:03
-	210	dmos and interconnect	USPAT	2003/10/24 16:47
-	25365	inverter adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 16:48
-	509	dmos and cmos	USPAT	2003/10/24 16:48
-	50	(vertical adj replacement adj gate (vrg and 257/\$3.ccls.) surround\$3 adj gate) and (inverter adj circuit) and @ad<20020825	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/10 15:05
-	6324	((257/327-331,334,369,379,380,385,401,900,903) or (365/154,180,184) or (438/138,152-156,206,268)).CCLS.	USPAT	2003/03/10 15:07
-	36	((vertical adj replacement adj gate (vrg and 257/\$3.ccls.) surround\$3 adj gate) and @ad<20000825) and (inverter adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/10 15:07
-	159	(dmos and interconnect) and @pd<19990801	USPAT	2003/03/10 15:07
-	16	(inverter adj circuit) and (dmos and cmos)	USPAT	2003/10/24 16:48
-	104	(dmos and interconnect) and (dmos and cmos) not ((inverter adj circuit) and (dmos and cmos))	USPAT	2003/03/10 15:07
-	16852	sram	USPAT	2003/10/24 16:48
-	57	"4670768"	USPAT	2003/10/24 16:48

-	1	("6518622").PN.	USPAT; US-PGPUB	2003/10/24 16:18
-	982	(257/351,508,509,904).CCLS.	USPAT	2003/10/24 16:27
-	17	((("5122848") or ("5136350") or ("5414288") or ("5414289") or ("5483094") or ("5504359") or ("5599724") or ("5707885") or ("5872374") or ("5994779") or ("6027975") or ("6049106") or ("6060746") or ("6104077") or ("6107133") or ("6127209") or ("6165823"))).PN.	USPAT; US-PGPUB	2003/10/24 16:19
-	1079	(257/351,508,509,904).CCLS.	USPAT; US-PGPUB	2003/10/24 16:45
-	0	((("5122848") or ("5136350") or ("5414288") or ("5414289") or ("5483094") or ("5504359") or ("5599724") or ("5707885") or ("5872374") or ("5994779") or ("6027975") or ("6049106") or ("6060746") or ("6104077") or ("6107133") or ("6127209") or ("6165823"))).PN.) and ((257/351,508,509,904).CCLS.)	USPAT; US-PGPUB	2003/10/24 16:21
-	528	((257/351,508,509,904).CCLS.) and cmos	USPAT; US-PGPUB	2003/10/24 16:21
-	154	((257/351,508,509,904).CCLS.) and cmos) and inverter	USPAT; US-PGPUB	2003/10/24 16:22
-	440	((257/351,508,509,904).CCLS.) and cmos) and (metal silicide)	USPAT; US-PGPUB	2003/10/24 16:26
-	399	((257/351,508,509,904).CCLS.) and cmos) and (metal silicide)	USPAT	2003/10/24 16:42
-	2350	(257/327-329,331,351,508,509,903,904).CCLS.	USPAT	2003/10/24 16:45
-	13	dmos and interconnect and @pd>20030309	USPAT	2003/10/24 16:49
-	29	dmos and cmos and @pd>20030309	USPAT	2003/10/24 16:48
-	1203	inverter adj circuit and @pd>20030309	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/24 16:48
-	1	(inverter adj circuit) and (dmos and cmos) and @pd>20030309	USPAT	2003/10/24 16:48
-	1737	sram and @pd>20030309	USPAT	2003/10/24 16:48
-	0	"4670768" and @pd>20030309	USPAT	2003/10/24 16:48
-	5	mosfet and monocrystalline adj gate	USPAT; US-PGPUB	2003/10/24 17:13
-	77	mosfet and monocrystalline near3 gate	USPAT; US-PGPUB	2003/10/24 17:13
-	72	(mosfet and monocrystalline near3 gate) not (mosfet and monocrystalline adj gate)	USPAT; US-PGPUB	2003/10/24 17:13